

United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandra, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/807,624	03/24/2004	Helmut Brech	SC13191ZP	4056
23125	7590 09/16/2005	5 EXAMINER		INER
FREESCALE SEMICONDUCTOR, INC.			QUACH, TUAN N	
LAW DEPARTMENT 7700 WEST PARMER LANE MD:TX32/PL02			ART UNIT	PAPER NUMBER
AUSTIN, T		2826		
			DATE MAILED: 09/16/200	5

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)				
Office Action Summary		10/807,624	BRECH, HELMUT				
		Examiner	Art Unit				
		Tuan Quach	2826				
	The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 1 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).							
Status							
1)	Responsive to communication(s) filed on		·				
,—	s action is FINAL . 2b) This action is non-final.						
3)							
,—	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Dispositi	on of Claims						
4)🖂	4) Claim(s) 1-27 is/are pending in the application.						
	4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.							
6)	6) Claim(s) is/are rejected.						
7) 🗌	7) Claim(s) is/are objected to.						
8) Claim(s) <u>1-27</u> are subject to restriction and/or election requirement.							
Applicati	on Pa <u>p</u> ers						
9) ☐ The specification is objected to by the Examiner.							
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.							
Priority ι	ınder 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of:							
1. Certified copies of the priority documents have been received.							
2. Certified copies of the priority documents have been received in Application No							
3. Copies of the certified copies of the priority documents have been received in this National Stage							
application from the International Bureau (PCT Rule 17.2(a)).							
* See the attached detailed Office action for a list of the certified copies not received.							
Attachmen	t(c)						
	e of References Cited (PTO-892)	4) Interview Summary	(PTO-413)				
2) Notic	e of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Da	ate				
	nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date	5) Notice of Informal P 6) Other:	atent Application (PTO-152)				

Restriction to one of the following inventions is required under 35 U.S.C. 121:

Page 2

I. Claims 1-10, 15-27, drawn to a semiconductor device, classified in class 257, subclass 288.

II. Claims 11-14, drawn to a method of making a semiconductor device, classified in class 438, subclass 197.

The inventions are distinct, each from the other because of the following reasons:

Inventions II and I are related as process of making and product made. The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). In the instant case the product as claimed can be made by another and materially different process wherein the gate pattern or finger is formed on a temporary substrate, followed by forming gate dielectric thereon, removing the resulting structure from the temporary substrate, joining it to the semiconductor substrate having an active area.

Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.

Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).

If applicant elects group I claims 1-10 and 15-27 above the following is further applicable. This application contains claims directed to the following patentably distinct species of the claimed invention:

Page 3

- 1. The species of the claimed invention in claims 24-27 regarding in a semiconductor device having contacts to gate tabs over an active region and gate fingers spaced from the active region by a gate dielectric of a first thickness and connected to the gate tabs, the improvement comprising tab insulators between the tab connections and the substrate having a thickness greater than the gate dielectric.
- 2. The species of the claimed invention in claims 15-23 regarding a semiconductor device comprising a semiconductor substrate, a gate finger spaced from the substrate by a gate dielectric having a first thickness, a first gate tab adjacent to the gate finger and spaced from the substrate by a first tab insulator having a second thickness; a second gate tab adjacent to the gate finger and spaced from the substrate by a second tab insulator having the second thickness; a first tab connection connected to the first gate tab and the gate finger; a second tab connection connected to the second gate tab; and a gate bus connected to the first and second gate tabs.
- 3. The species of the claimed invention in claims 1-10 regarding a semiconductor device, comprising an active region in a semiconductor substrate, a gate finger over a channel in the active region and spaced from the channel by a gate dielectric having a first thickness, wherein the gate finger is of a first material, a first gate tab adjacent to the gate finger, over the active region and spaced from the substrate by a first tab insulator having a second thickness, wherein the first gate tab is of the first

material and the second thickness is greater than the first thickness, wherein the first material is conductive, a second gate tab adjacent to the gate finger, over the active region and spaced from the substrate by a second tab insulator having the second thickness, wherein the second gate tab is of the first material, a first tab connection electrically connecting the first gate tab to the gate finger, wherein the first tab connection is of the first material, a second tab connection electrically connecting the second gate tab to the gate finger, wherein the second gate tab is of the first material, and a gate bus electrically connected to the first and second gate tabs..

Applicant is required under 35 U.S.C. 121 to elect a single disclosed species for prosecution on the merits to which the claims shall be restricted if no generic claim is finally held to be allowable. Currently, no claim is generic.

Applicant is advised that a reply to this requirement must include an identification of the species that is elected consonant with this requirement, and a listing of all claims readable thereon, including any claims subsequently added. An argument that a claim is allowable or that all claims are generic is considered nonresponsive unless accompanied by an election.

Upon the allowance of a generic claim, applicant will be entitled to consideration of claims to additional species which are written in dependent form or otherwise include all the limitations of an allowed generic claim as provided by 37 CFR 1.141. If claims are added after the election, applicant must indicate which are readable upon the elected species. MPEP § 809.02(a).

Should applicant traverse on the ground that the species are not patentably distinct, applicant should submit evidence or identify such evidence now of record showing the species to be obvious variants or clearly admit on the record that this is the case. In either instance, if the examiner finds one of the inventions unpatentable over the prior art, the evidence or admission may be used in a rejection under 35 U.S.C. 103(a) of the other invention.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to examiner Tuan Quach whose telephone number is 571-272-1717. The examiner can normally be reached on M-F from 8:30 AM to 4:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor Nathan Flynn, can be reached on 571-272-1915. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tuan Quach
Primary Examiner